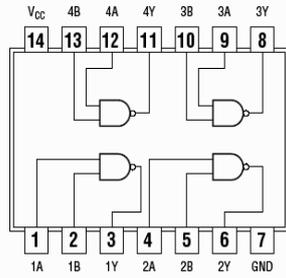


00

**QUADRUPLE 2-INPUT
POSITIVE-NAND GATES**

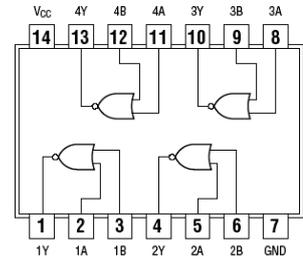
positive logic:
 $Y = A \cdot B$



02

**QUADRUPLE 2-INPUT
POSITIVE-NOR GATES**

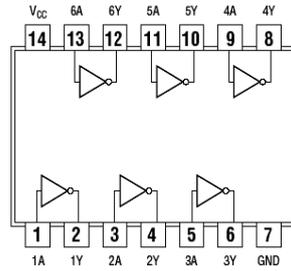
positive logic:
 $Y = A + B$



04

HEX INVERTERS

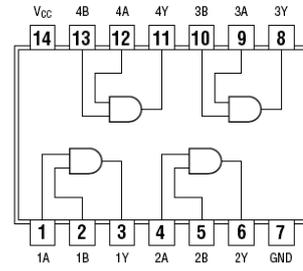
positive logic:
 $Y = \overline{A}$



08

QUADRUPLE 2-INPUT POSITIVE-AND GATES

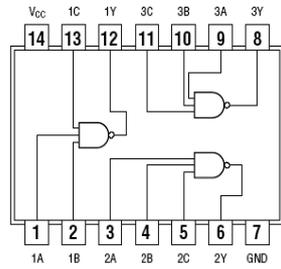
positive logic:
 $Y = A \cdot B$



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**TRIPLE 3-INPUT
POSITIVE-NAND GATES**

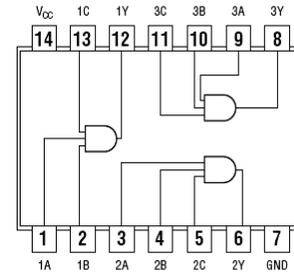
positive logic:
 $Y = A \cdot B \cdot C$



11

**TRIPLE 3-INPUT
POSITIVE-AND GATES**

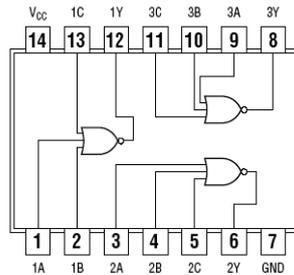
positive logic:
 $Y = A \cdot B \cdot C$



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**TRIPLE 3-INPUT
POSITIVE-NOR GATES**

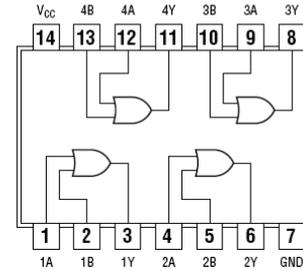
positive logic:
 $Y = \overline{A + B + C}$



32

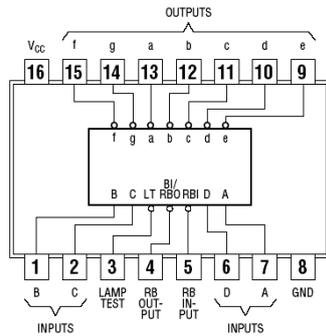
**QUADRUPLE 2-INPUT
POSITIVE OR GATES**

positive logic:
 $Y = A + B$



47

BCD-TO-SEVEN-SEGMENT DECODERS/DRIVERS

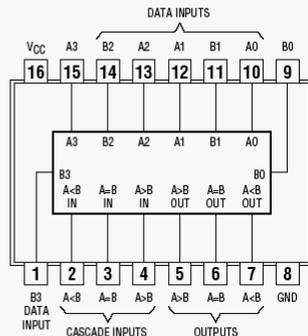


FUNCTION TABLE

No.	INPUTS					B/RBO	OUTPUTS							
	LT	RBI	D	C	B		A	a	b	c	d	e	f	g
0	H	H	L	L	L	L	H	ON	ON	ON	ON	ON	ON	OFF
1	H	X	L	L	L	H	H	OFF	ON	OFF	OFF	OFF	OFF	OFF
2	H	X	L	L	H	L	H	ON	ON	OFF	ON	OFF	OFF	ON
3	H	X	L	L	H	H	H	ON	ON	ON	ON	OFF	OFF	ON
4	H	X	L	H	L	L	L	OFF	ON	ON	OFF	OFF	OFF	ON
5	H	X	L	H	L	H	H	ON	OFF	ON	ON	OFF	ON	ON
6	H	X	L	H	H	L	H	OFF	OFF	ON	ON	ON	ON	ON
7	H	X	L	H	H	H	H	ON	ON	ON	ON	OFF	OFF	OFF
8	H	X	H	L	L	L	L	ON						
9	H	X	H	L	L	H	H	ON	ON	ON	OFF	OFF	ON	ON
10	H	X	H	L	H	L	H	OFF	OFF	OFF	ON	ON	OFF	ON
11	H	X	H	L	H	H	H	OFF	OFF	ON	ON	OFF	OFF	ON
12	H	X	H	H	L	L	L	OFF	ON	OFF	OFF	OFF	ON	ON
13	H	X	H	H	L	H	H	ON	OFF	OFF	ON	OFF	ON	ON
14	H	X	H	H	H	L	H	OFF	OFF	OFF	ON	ON	ON	ON
15	H	X	H	H	H	H	H	OFF						
BI	X	X	X	X	X	X	L	OFF						
RBI	H	L	L	L	L	L	L	OFF						
LT	L	X	X	X	X	X	H	ON						

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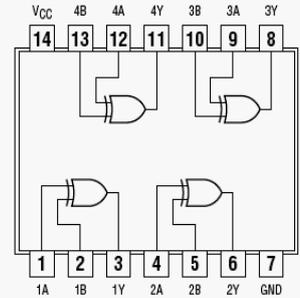
4-BIT MAGNITUDE COMPARATORS



86

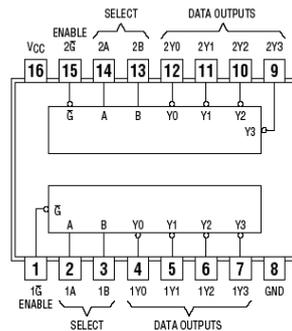
QUADRUPLE 2-INPUT EXCLUSIVE-OR GATES

positive logic:
 $Y = A \oplus B$ or $Y = \bar{A}B + A\bar{B}$



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DUAL 2-TO-4-LINE DECODERS/DEMULPLEXERS

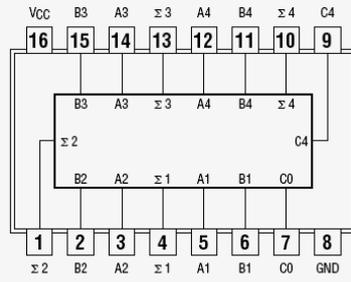


FUNCTION TABLE

INPUTS		OUTPUTS			
ENABLE	SELECT	Y0	Y1	Y2	Y3
H	X	X	H	H	H
L	L	L	L	H	H
L	L	L	H	L	H
L	H	L	H	H	L
L	H	H	H	H	L

283

4-BIT BINARY FULL ADDERS



266

QUAD 2-INPUT EXCLUSIVE-NOR GATES WITH OPEN-COLLECTOR OUTPUTS

positive logic:
 $Y = A \oplus B$

