

Exp3: Multiplexer

Objectives:

The main goal of this experiment is how to create a multiplexer using VHDL.

Logic Design:

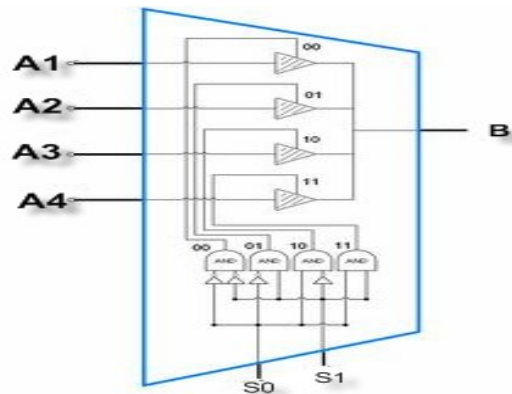


Figure (3-1): 4 x 1 Multiplexer.

Assigning Pins with Constraints:

Signals		Element on XSA-200 Board	XC2S200 FPGA Pin
Input	A1	Sw1-1	P11
	A2	Sw1-2	M10
	A3	Sw1-3	R11
	A4	Sw1-4	N10
	S0	D0	E13
	S1	D1	C16
Output	B	Segment A	G16